

Claims

What is claimed is;

1. A method for dynamic compiling, comprising:
loading byte-code on a digital information appliance, said byte-code suitable for
including a tagged section;
identifying the tagged section of the byte-code; and
compiling the tagged section of byte-code;
wherein the tagged section is compiled when the byte-code is loaded so as to enable
the digital information appliance to utilize the tagged section of byte-code without additional
compiling of the tagged section of byte-code by the digital information appliance.

2. The method as described in claim 1, further comprising:
encoding application source code to byte-code, the byte-code including code in a
processor-independent form which is suitable for further analysis; and
tagging a section of the byte-code.

3. The method as described in claim 2, wherein the byte-code including code in
the processor-independent form is suitable for further analysis including at least one of
suitable for compiler optimization, suitable for processing by interpreters, and suitable for
use in generation of binary instruction for the digital information appliance processing
system.

4. The method as described in claim 2, wherein the section of the byte-code is
tagged for being performance sensitive.

5. The method as described in claim 1, further comprising storing the compiled
tagged section of byte-code in persistent storage.

6. The method as described in claim 1, wherein loading includes validating that the byte-code conforms with byte-code suitable for utilization by the digital information appliance.

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1 7. ~~A digital information appliance suitable for dynamic compiling, comprising:~~
2 ~~a processor for implementing a program of instructions; and~~
3 ~~a memory for storing the program of instructions, the program of instructions suitable~~
4 ~~for configuring the digital information appliance to~~
5 ~~load byte-code, said byte-code suitable for including a tagged section;~~
6 ~~identify the tagged section of the byte-code; and~~
7 ~~compile the tagged section of byte-code;~~
8 ~~wherein the tagged section is compiled when the byte-code is loaded so as to enable~~
9 ~~the digital information appliance to utilize the tagged section of byte-code without additional~~
10 ~~compiling of the tagged section of byte-code by the digital information appliance.~~

1 8. The digital information appliance as described in claim 7, further comprising:
2 encoding application source code to byte-code, the byte-code including code in a
3 processor-independent form which is suitable for further analysis; and
4 tagging a section of the byte-code.

1 9. The digital information appliance as described in claim 8, wherein the byte-
2 code including code in the processor-independent form is suitable for further analysis
3 including at least one of suitable for compiler optimization, suitable for processing by
4 interpreters, and suitable for use in generation of binary instruction for a digital information
5 appliance's processing system.

1 10. The digital information appliance as described in claim 7, wherein the section
2 of the byte-code is tagged for being performance sensitive.

1 11. The digital information appliance as described in claim 7, further comprising
2 storing the compiled tagged section of byte-code in persistent storage.

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1 13. A system for providing an execution environment that is suitable for dynamic
2 compiling, comprising:

3 a memory device suitable for storing computer readable information;

4 a loader coupled to the memory device, the loader suitable for loading byte-code to
5 the memory, said byte-code suitable for including a tagged section;

6 an identifier coupled to the loader, the identifier suitable for identifying the tagged
7 section of the byte-code;

8 a compiler coupled to the identifier;

9 wherein the identified tagged section is compiled by the compiler when the byte-code
10 is loaded so as to enable the tagged section of byte-code to be utilized without additional
11 compiling of the tagged section of byte-code.

12 14. The system as described in claim 13, further comprising:

13 an encoder for encoding application source code to byte-code, the byte-code
14 including code in a processor-independent form which is suitable for further
15 analysis; and

16 a tagger for tagging a section of the byte-code.

17 15. The system as described in claim 14, wherein the byte-code including code
18 in the processor-independent form is suitable for further analysis including at least one of
19 suitable for compiler optimization, suitable for processing by an interpreter, and suitable for
20 use in generation of binary instruction for a processing system.

21 16. The system as described in claim 14, wherein the section of the byte-code is
22 tagged for being performance sensitive.

23 17. The system as described in claim 13, wherein the loader includes a validation
24 utility for validating the byte-code conforms with byte-code suitable for utilization by the
25 system.

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2 *as* 18. A method for providing an execution environment in an information appliance
3 network, comprising:
4 (a) encoding an application source code in a processor independent byte-code;
5 (b) tagging at least some portion of said processor independent byte-code; and
(c) compiling at least some portion of said tagged processor independent byte-code.

1 19. The execution environment for an information appliance network of claim 18,
2 further including validating at least some portion of said processor independent byte-code.

1 20. The execution environment for an information appliance network of claim 18,
2 wherein compiling includes identifying the portion of said tagged processor independent
3 byte-code.